**Date: 9/20/2014**

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| **Group Number and Name** | **#14 Low Cost RF power meter** |
| **Client/Advisor** | **Dan Stieler/ Prof Neihart** |
| **Attendees/Role** | **Silu Feng/Leader, Xiaoshuo Li/key idea holder, Yijia Huang/webmaster, Boyang Hu,Cong Han/communicater, Yusi Xie/key idea holder** |

# Past week accomplishments

What was done, who did it, and when it was done

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| * The diode group using transing simulation to test the diode detector.
* The gain of scale amp is doubled to make sure the correct output when using pulse input. A 10V voltage regulator is designed for op amp.
* The layout of PCB board is designing in eagle software.
* The coupler group build and test the coupler by using power supply and oscilloscope.
* The coupler group built the prototype directional coupler by hand.
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# Plan for coming week

What to do, who, and when should it be done

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| * The coupler group needs to measure the coupler with voltmeter (including AC sine and RMS value).
* The coupler group needs to use cursor to measure the value.
* The coupler group need to figure out how to put the build one into the box.
* The diode group needs to add voltage regulator in the simulaton.
* The diode group needs to put the circuit on PCB board.
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# Pending Issues

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| * Build and test the directional coupler.
* Finish the design of diode detector.
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# Individual Contributions

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| * Silu Feng work with Yijia Huang to build and test the coupler. (2 hr). Prepare the material list (0.5 hr).
* Xiaoshuo Li use ADS to test diode detector. Revise the simulation. Research how to build a square unit. (8 hr)
* Yijia Huang testing coupler check, cable cut, PC board cut and get new BNC connectors (3 hr). Rebuild testing coupler due to change turns of the toroids (1.5 hr).
* Boyang Hu search how to design voltage regulator for op amp (3 hr). Design offset voltage supply for scaling amplifier(2 hr). Search parts for 10 position switch and recalculate the new gain for pulse input. (1.5 hr)
* Cong Han viewed the tutorials of eagle printed board circuit (3 hr). started downloading and installing the Eagle print board circuit (0.5 hr). Created folder and sample schematic (1hr) looked up real digiKey part number for low pass filter(1 hr). Learned how to add library and measure dimensions (2 hr). Watched more tutorial of how to measure dimension more specifically(0.5 hr).
* Yusi Xie work with Yijia Huang to build and test the coupler(1.5 hr). Build the prototype of coupler(0.5 hr).
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Individual hourly Contributions

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| **NAME** | **Hours this week** | **HOURS****Cumulative** |
| Silu Feng | 2.5 | 11.5 |
| Xiaoshuo Li | 8 | 19 |
| Yijia Huang | 4.5 | 18 |
| Boyang Hu | 6.5 | 16.5 |
| Cong Han | 8 | 18 |
| Yusi Xie | 2 | 11 |

# Comments and extended discussions:

We all did a great job in the past week.