**Date: 9/13/2014**

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| **Group Number and Name** | **#14 Low Cost RF power meter** |
| **Client/Advisor** | **Dan Stieler/ Prof Neihart** |
| **Attendees/Role** | **Silu Feng/Leader, Xiaoshuo Li/key idea holder, Yijia Huang/webmaster, Boyang Hu,Cong Han/communicater, Yusi Xie/key idea holder** |

# Past week accomplishments

What was done, who did it, and when it was done

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| * The diode group use AC input to test the low pass filter.
* The gain of scaling amplifier is multiplied by square root of 2 to satisfy the square input.
* The coupler group bought connectors and 2W resistors to build the coupler.
* The coupler group built the directional coupler by hand.
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# Plan for coming week

What to do, who, and when should it be done

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| * The coupler group needs to buy two more connectors to build the piece.
* The coupler group needs to test the build one as soon as possible.
* The coupler group need to figure out how to put the build one into the box.
* The diode group needs to finish the revise for low pass filter and scaling amplifier.
* The diode group needs to put the circuit on PCB board.
* The scaling amplifier needs to add an offset voltage supply.
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# Pending Issues

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| * Build and test the directional coupler.
* Finish the design of diode detector.
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# Individual Contributions

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| * Silu Feng work with Yijia Huang to build the coupler. (2 hr). Search and design how to test directional coupler (3 hr).
* Xiaoshuo Li use ADS to test diode detector. Work with Cong Han and Boyang Hu to revise the design of diode detector. Add pulse input to test the system. (6 hr)
* Yijia Huang draw the schematic about testing coupler. Prepare for building testing coupler( cable cut, sheath remove, PC board cut, two 50ohm resistors and Tin soldering). (3hr) Build testing coupler and check(solder). (5hr) Communicate with teammate about testing. (0.5hr)
* Boyang Hu work with Xiaoshuo to solve the problem caused by pulse input. Recalculate the gain of the scaling amplifier. Add another set of resistors to meet the demand of pulse input. (6 hr)
* Cong Han review the documents from last semester. (1 hr) Redraw the whole system with block diagram and draw the wave form from the beginning to the end. (1hr) Did an example of calculation from beginning to end.(1hr) Change the value of resistance of low pass filter in the simulation to get better result.(3hr)
* Yusi Xie work with Yijia Huang to build the coupler. Search how to cut the board. Search how to test directional coupler. (5hr)
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Individual hourly Contributions

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| **NAME** | **Hours this week** | **HOURS****Cumulative** |
| Silu Feng | 5 | 9 |
| Xiaoshuo Li | 6 | 11 |
| Yijia Huang | 8.5 | 13.5 |
| Boyang Hu | 6 | 10 |
| Cong Han | 6 | 10 |
| Yusi Xie | 5 | 9 |

# Comments and extended discussions:

We all did a great job in the past week.